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Bib Data Sheet

CONFIRMATION NO. 2168

SERIAL NUMBER 10/662,384	FILING DATE 09/16/2003 RULE	CLASS 257	GROUP ART UNIT 2826	ATTORNEY DOCKET NO. 031111
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** CONTINUING DATA ***** *AS* *none*

** FOREIGN APPLICATIONS ***** *AS*
 JAPAN 2002-270251 09/17/2002 NO

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 12/08/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 9
Verified and Acknowledged	<i>AS</i> Examiner's Signature _____ Initials _____				

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TITLE
 Semiconductor device, manufacture and evaluation methods for semiconductor device, and process condition evaluation method

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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